



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ryo NAKAGAKI et al.

Application No.: 10/699,528

Filed: October 31, 2003

For: Method and System for Manufacturing  
a Semiconductor Device

Customer No.: 20350

Confirmation No. 4228

Examiner: Kin-Chan Chen

Technology Center/Art Unit: 1765

AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed September 27, 2005, on the above-referenced application, please enter the following amendments and remarks:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the Listing of Claims which begins on page 3 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.